

## OPEN POSITION

Replisaurus Technologies is a semiconductor technology company commercializing the patented ECPR™ process used for printing of micro- and nanoscale metal patterns. The highly accurate metallization process enables simplified and cost efficient fabrication of next generation microelectronic and semiconductor devices. We are now expanding our organization and are looking for new key members to our team. If you are a high aiming person with relevant background looking for new challenges, please submit your CV and Cover Letter to Sabine Scherer at Wellington Partners. ([scherer@wellington-partners.com](mailto:scherer@wellington-partners.com))

### **SENIOR PROCESS INTEGRATION AND YIELD ENGINEER – Master Fabrication** (Kista, Sweden)

#### **Job description**

As a SENIOR PROCESS INTEGRATION AND YIELD ENGINEER you will be a part of the master fabrication team of the ECPR™ (ElectroChemical Pattern Replication) technology, used for production of micro- and nanoscale metal patterns in semiconductor and electronic components. The work involves creating, optimizing, and integrating advanced wafer processes, such as thin film deposition, patterning, etching and metallization steps with a focus on device performance characterization and measurement techniques using automated inspection and metrology tools. You will interact with the process and tool development teams of Replisaurus and work closely with tool and material suppliers.

#### **Tasks and responsibilities**

- Primary ownership of automated inspection and metrology tools. (KLA AIT, Nikon Optistation, Confocal laser review system)
- Developing and optimizing protocols, procedures and recipes for inspection and metrology data collection as well as data management and analysis using process database solutions.
- Film and material engineering to optimize mechanical, electrical and surface properties of master electrodes aiming at improved print performance and lifetime as well as minimum defectivity.
- Process integration / flow engineering for improved line yields and reduced defectivity.
- Developing, optimizing and integrating process steps by modifying tools and chemistry, designing experimental plans, executing experiments, and collecting data.
- Thoroughly analyzing data and drawing statistically valid conclusions for experiments.
- Generating internal documentation and process engineering specifications.
- Providing regular project status updates to management and the rest of the master team.
- Demonstrating a “start-up- mentality” and a willingness to work until success is achieved.

#### **Qualifications**

- Advanced degree (M.Sc. or Ph.D.) with focus on MEMS or microelectronics
- 5+ years of hands-on experience from development within semiconductor wafer processes
- Good understanding of wafer process equipments as well as metrology and inspection methods.
- Proficiency in use of dark and bright field inspection tools, review stations, optical microscopes, SEMs, surface profilers, confocal microscopes, AFMs, and electrical probers.
- Excellent skills in statistical design of experiments (DOE), data collection, data management and analysis using statistical software (e.g., JMP).
- Strong analytical skills to work on problems with diverse scope
- Ability to work effectively within a team and independently as needed
- Excellent oral and written communication skills (English)